

IL920020007US1

The listing of claims begins on page 2 of this paper. Remarks begin on page 4 of this paper.

This listing of claims will replace all prior versions including the listing of claims in the application.

Listing of claims:

1 - 41 (Cancelled)

42. (new) A method comprising:

when designing an integrated circuit operatable at both DC and AC frequencies, selecting one of a set of transmission line topologies for critical interconnect lines capable of carrying high frequency signals.

43. (new) An integrated circuit design library comprising:

a set of transmission line topologies for critical interconnect lines capable of carrying DC and AC signals, which topologies comprise return paths therein; and
a set of parametrized, equivalent RLC ladder networks, one per topology.

44. (new) The method according to claim 4 and also comprising means for performing time domain analysis for each transmission line placed into an integrated circuit design.

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2